

(et No.: 500.40501*00 Serial No.: 09/931, 250 W.S. OFFICE

Atty. Docket No.: 500.40501%00

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:

Fumikazu Takahashi et al.

Serial No.:

09/931,250

Filed:

August 17, 2001

For:

SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

Art Unit:

2816

Examiner:

Cassandra F. Cox

<u>AMENDMENT</u>

Assistant Commissioner of Patents Washington, D.C. 20231

August 12, 2002

Sir:

In response to the Office Action dated April 10, 2002, please amend the above-identified application as follows:

IN THE CLAIMS:

Please amend claims 1 and 6 as follows:

1. (Amended) A semiconductor integrated circuit device comprising a driver circuit, a first long-distance wiring connected to the driver circuit, and a plurality of gate circuits connected over the entire length of the first long-distance wiring, so that An input signal VIN is received by the plurality of gate circuits via the driver circuit and the first long-distance wiring,

wherein a node arranged in the vicinity of an input terminal of the gate circuit connected to an end of the first long, distance wiring and an input terminal of the

08/14/2002 AUSHAN1 00000015 09931250

02 FC:102

336.00 OP